

## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
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APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
09/929,542	08/13/2001	Hieu Van Tran	2102397-991720

CONFIRMATION NO. 9808

## FORMALITIES LETTER



\*OC00000006501000\*

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1755 Embarcadero Road  
Palo Alto, CA 94303-3340

Date Mailed: 08/31/2001

## NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

FILED UNDER 37 CFR 1.53(b)

*Filing Date Granted*

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is unsigned.
- To avoid abandonment, a late filing fee or oath or declaration surcharge as set forth in 37 CFR 1.16(e) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.
- **The balance due by applicant is \$ 130.**

The application is informal since it does not comply with the regulations for the reason(s) indicated below.

The required item(s) identified below must be timely submitted to avoid abandonment:

- Substitute drawings in compliance with 37 CFR 1.84 because:
  - drawing sheets do not have the appropriate margin(s) (see 37 CFR 1.84(g)). Each sheet must include a top margin of at least 2.5 cm. (1 inch), a left side margin of at least 2.5 cm. (1 inch), a right side margin of at least 1.5 cm. ( 5/8 inch), and a bottom margin of at least 1.0 cm. (3/8 inch);

10/22/2001 CCHAU1 00000041 09929542

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*A copy of this notice **MUST** be returned with the reply.*

Customer Service Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

11/11/2011 11:11:11 AM



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hieu Van Tran et al.

Serial No.: 09/929,542

Art Unit: 2824

Filed: August 13, 2001

Examiner: Not yet known

Title: **ARRAY ARCHITECTURE AND OPERATING METHOD FOR DIGITAL  
MULTILEVEL NONVOLATILE MEMORY INTEGRATED CIRCUIT SYSTEM**

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as FIRST CLASS MAIL in an envelope addressed to: **BOX MISSING PARTS**, Assistant Commissioner for Patents, Washington, D.C. 20231, on October 16, 2001.

Rosa A. Caviedes

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**TRANSMITTAL OF FORMAL DRAWINGS**

**ATTN: OFFICIAL DRAFTSPERSON**

Asst. Commissioner for Patents

Washington, DC 20231

Dear Sir:

Transmitted herewith are forty-eight (48) sheets of formal drawings for the above-identified application.

Please address all future communications regarding this application to:

Edward B. Weller  
GRAY CARY WARE FREIDENRICH LLP  
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Please direct all telephone calls to Edward B. Weller at (650) 833-2436.

Please charge any additional fees, including any fees necessary for extensions of time, or credit overpayment to Deposit Account No. 07-1896. A copy of this sheet is enclosed for accounting purposes.

Respectfully submitted,

GRAY CARY WARE & FREIDENRICH LLP

Dated: October 16, 2001

By   
*Edward B. Weller*  
Reg. No. 37,468  
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